

PATENT  
P15727  
0077.0016

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: ) Examiner: Not Yet Assigned  
D.R. Gaur )  
Serial No.: 10/612,694 )  
Filed: July 2, 2003 ) Art Unit: 2183  
For: METHOD, SYSTEM, AND )  
PROGRAM FOR HANDLING )  
DEVICE INTERRUPTS IN A )  
MULTI-PROCESSOR )  
ENVIRONMENT )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

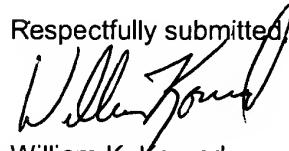
TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application. Applicants submit that no fee is needed nor should be charged with the submittal of this Information Disclosure Statement (IDS) because this IDS is being submitted under 1.97(b) in that the IDS is being filed before the mailing date of a first Office Action on the merits or within three months of the filing date of a national application other than a continued prosecution application under 1.53(d). Applicants submit that a first Office Action has not been received in the above referenced case.

Nonetheless, should any fee be deemed necessary, please charge any insufficiency or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this letter is enclosed for fee processing.

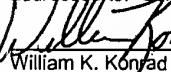
Respectfully submitted,



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Dated: April 20, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 20, 2004.



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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
(use as many sheets as necessary)

				<i>Complete if Known</i>	
				Application Number	10/612,694
				Filing Date	July 2, 2003
				First Named Inventor	D.R. Gaur
				Art Unit	2183
				Examiner Name	Not Yet Assigned
Page	1	of	1	Attorney Docket Number	P15727

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number- Kind Code (if known)			
		US-			
		US-			
		US-			

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Country Code- Number- Kind Code				

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
/A.H./		CORNELL THEORY CENTER (CTC), "Functions for Pinning Tasks to Processors", [online], © 2003 Cornell University, [retrieved on March 11, 2003]. Retrieved from the Internet at <a href="http://www.tc.cornell.edu/Services/Docs/Tips_pinning.htm">URL: http://www.tc.cornell.edu/Services/Docs/Tips_pinning.htm</a> .	
/A.H./		WEBOPEDIA.COM, "Definitions", [online], [retrieved on July 2, 2003]. Retrieved from the Internet at <a href="http://www.webopedia.com/">URL: http://www.webopedia.com/</a> .	

Examiner Signature	/Andy Ho/	Date Considered	09/27/2008
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